

Electronic Acknowledgement Receipt

EFS ID:	2021321
Application Number:	10633956
International Application Number:	
Confirmation Number:	5171
Title of Invention:	DEVICE FOR MARGIN TESTING A SEMICONDUCTOR MEMORY BY APPLYING A STRESSING VOLTAGE SIMULTANEOUSLY TO COMPLEMENTARY AND TRUE DIGIT LINES
First Named Inventor/Applicant Name:	Danial S. Dean
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Filer Authorized By:	James R. Duzan
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Application Type:	Utility under 35 USC 111(a)

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1	Request for Certificate of Correction	Request_for_Certificate_of_Correction.pdf	71413 5aec96b24340f8fe4cf1da3a012cfc6241e0bf09	no	3

Warnings:

Information:	
Total Files Size (in bytes):	71413
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